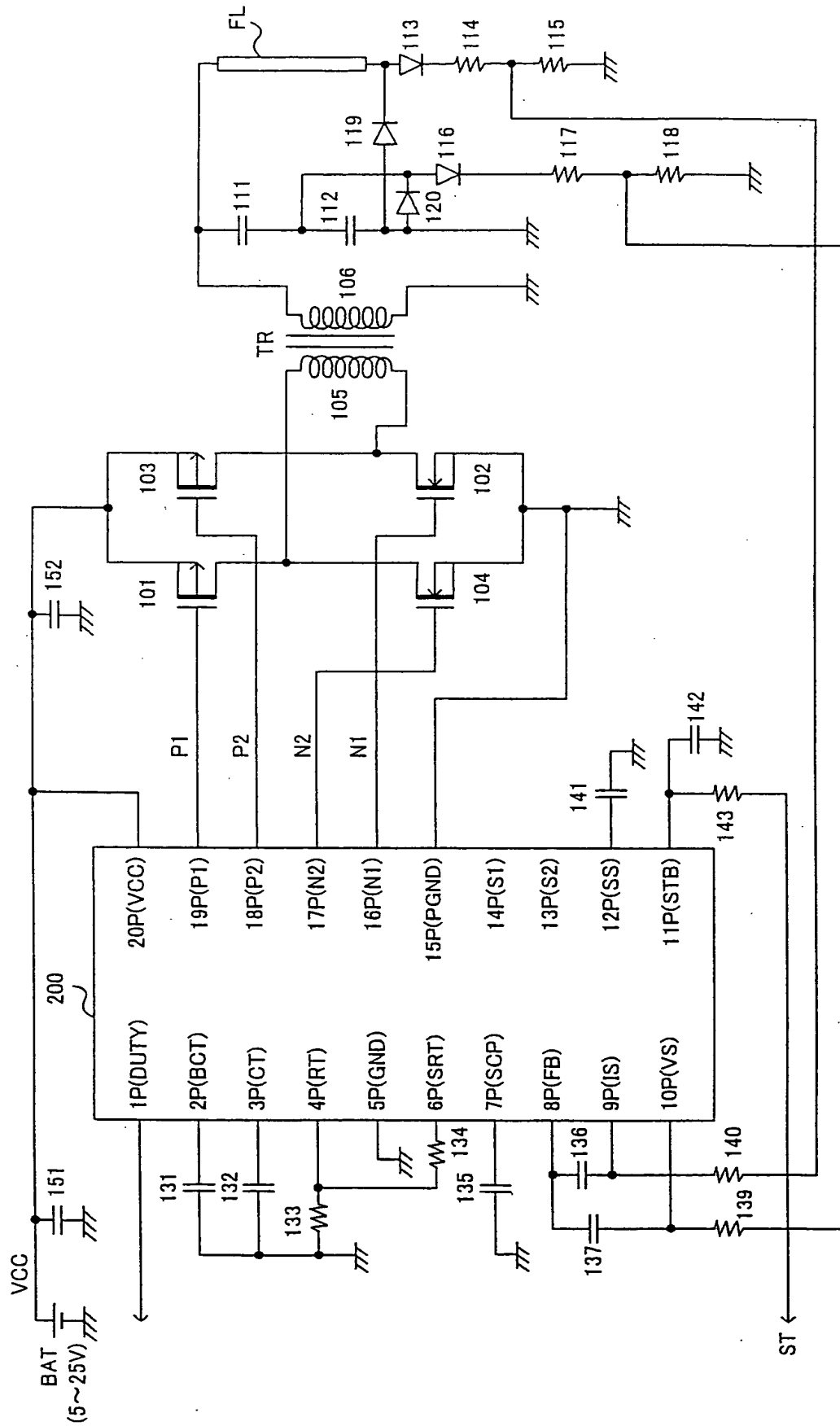


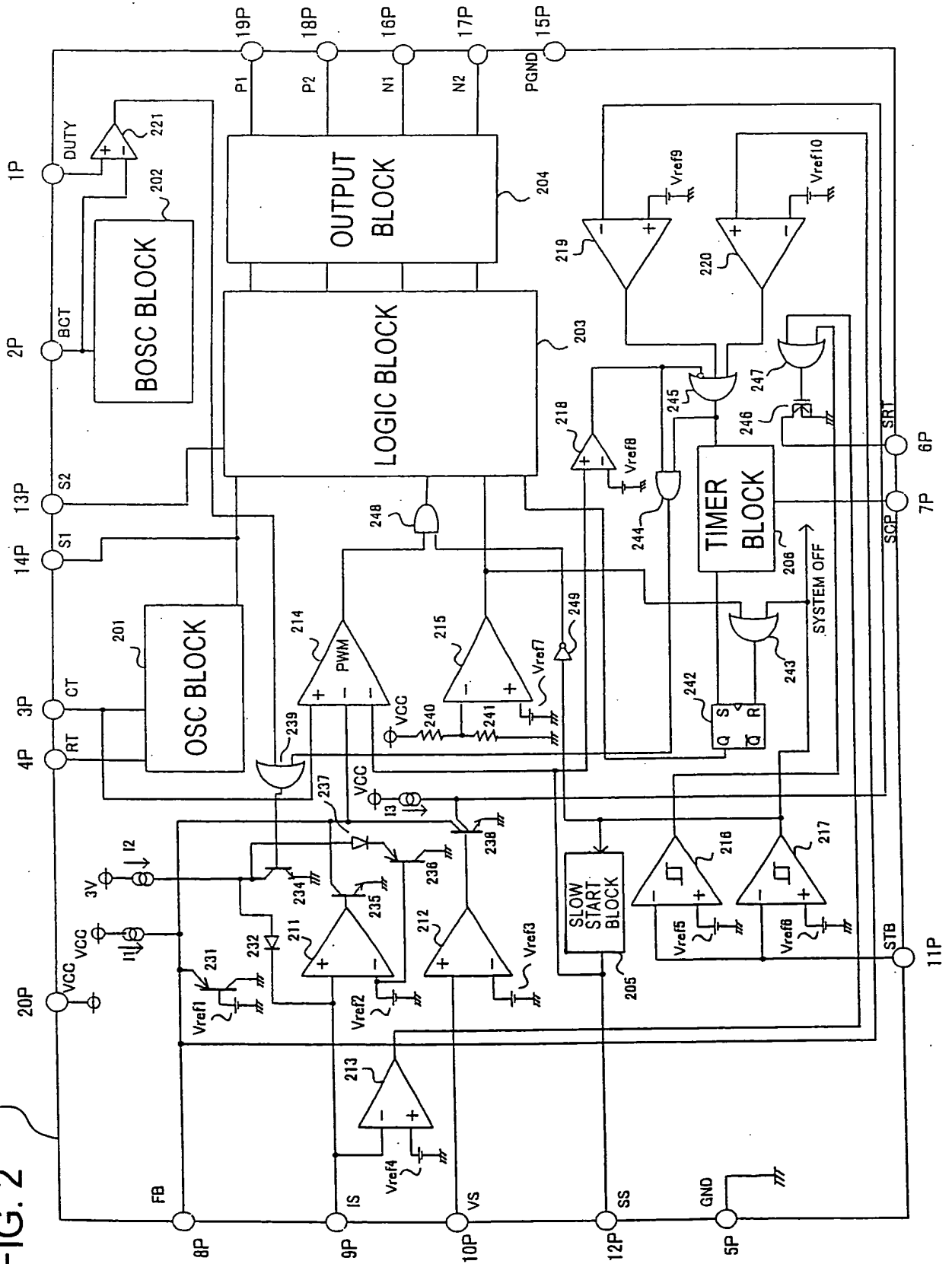
10/501579

FIG. 1



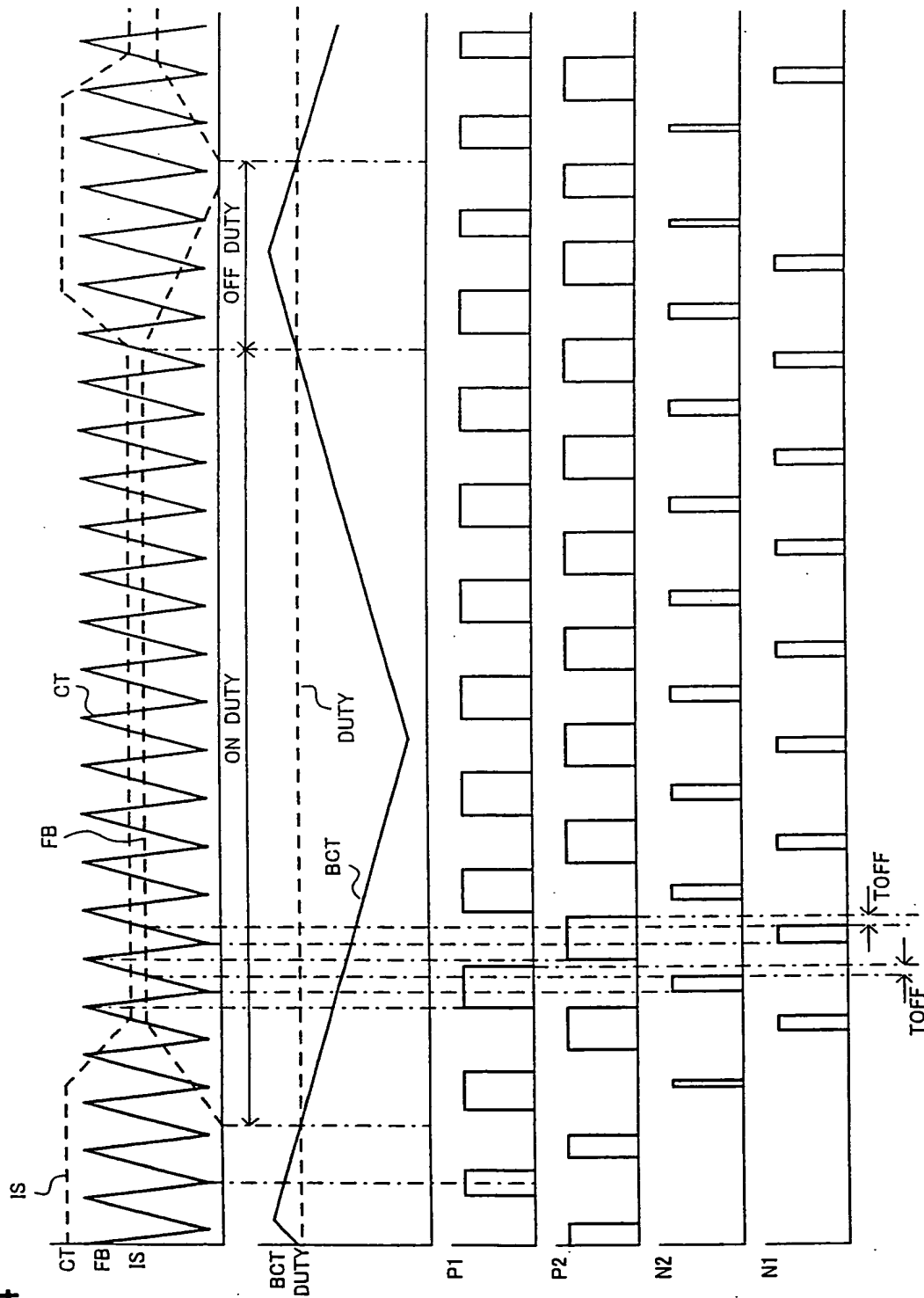
10/501579

FIG. 2



The circuit diagram illustrates a power supply system. It features a VCC input with current I_1 and a 3V input with current I_2 . The system includes a feedback loop (FB) with a capacitor 136 and a reference voltage V_{ref1} (231). A diode 232 and a transistor 234 are part of the feedback path. The output voltage V_o is regulated by a feedback network (137, 139, 140) and a feedback signal (SS). The system also includes a BOSC BLOCK (202) and an OSC BLOCK (201). The BOSC BLOCK is connected to a duty cycle input (DUTY) and a feedback input (FB). The OSC BLOCK is connected to a feedback input (FB) and a feedback signal (SS). The output of the BOSC BLOCK is connected to a feedback signal (SS) and a feedback input (FB). The output of the OSC BLOCK is connected to a feedback signal (SS) and a feedback input (FB). The system also includes a SLOW START BLOCK (205) and a feedback signal (SS).

FIG. 4



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FIG. 5

